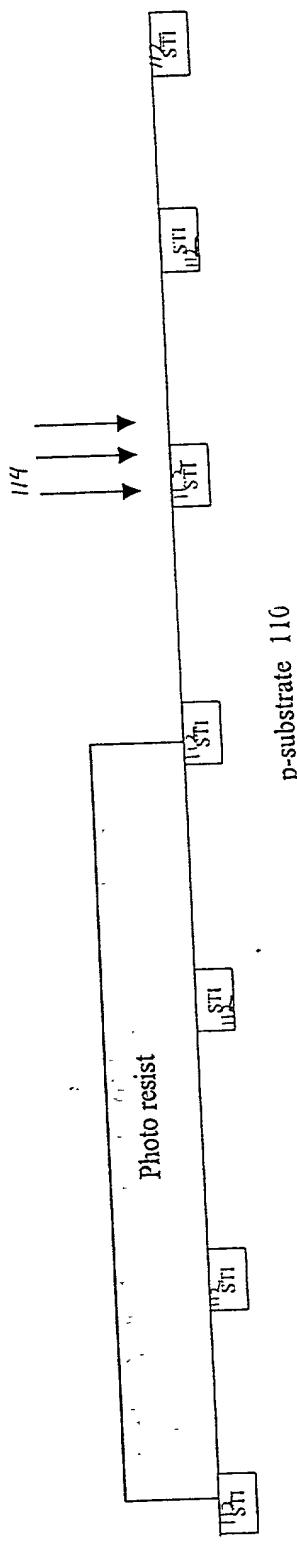


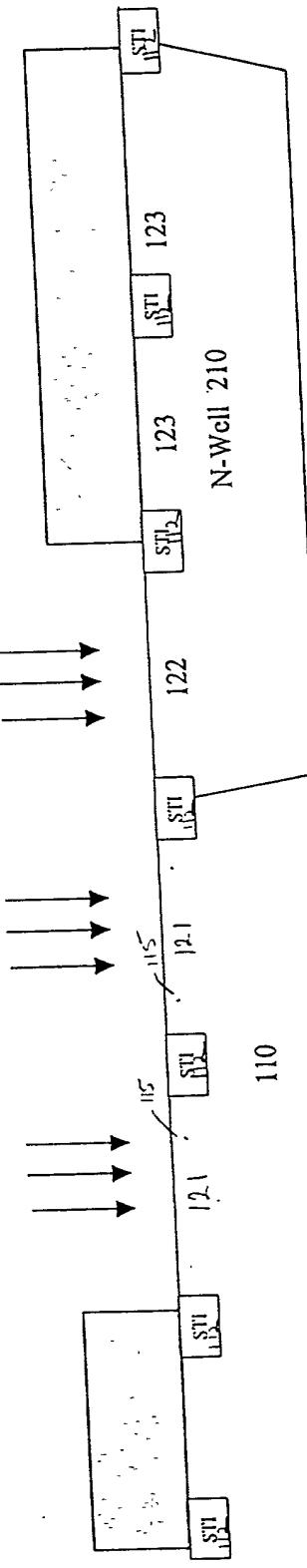
N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
$\beta_{2,1}$	$\beta_{2,1}$	$\beta_{2,1}$	$\beta_{2,2}$	$\beta_{2,2}$	$\beta_{2,2}$

Fig. 1



p-substrate 110

Fig. 2



N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Buried Channel	P-Poly PMOS Buried Channel
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Fig. 3

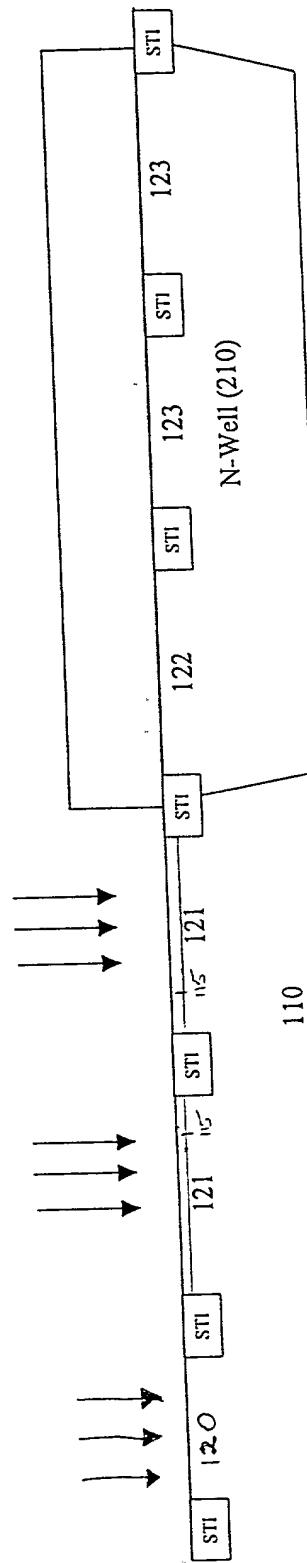
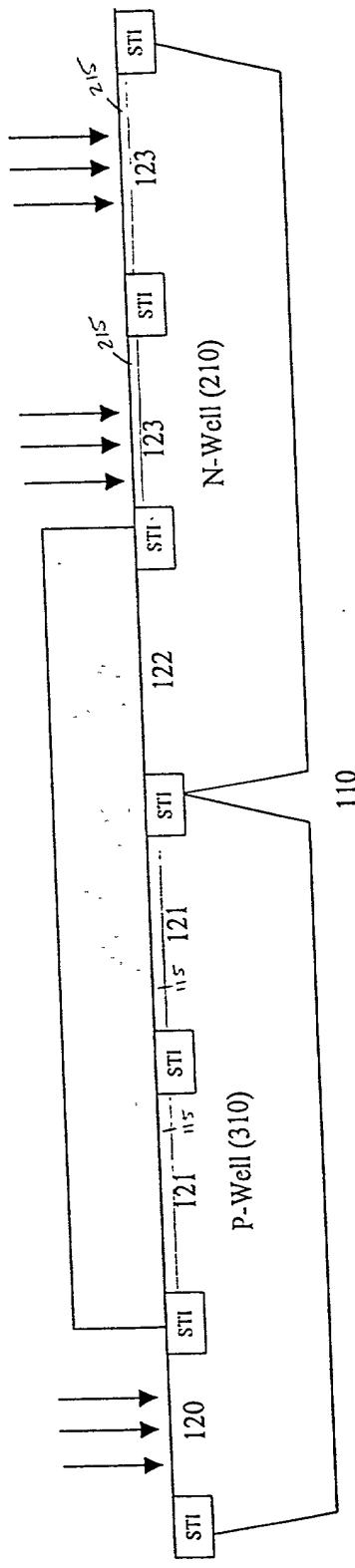


Fig. 4



# T E C H N I Q U E S

N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
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Fig. 5

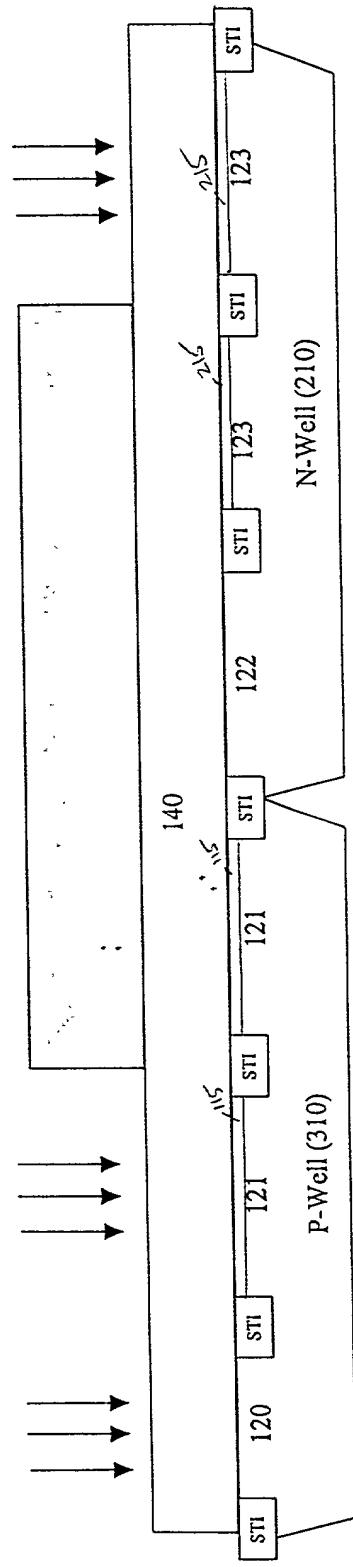
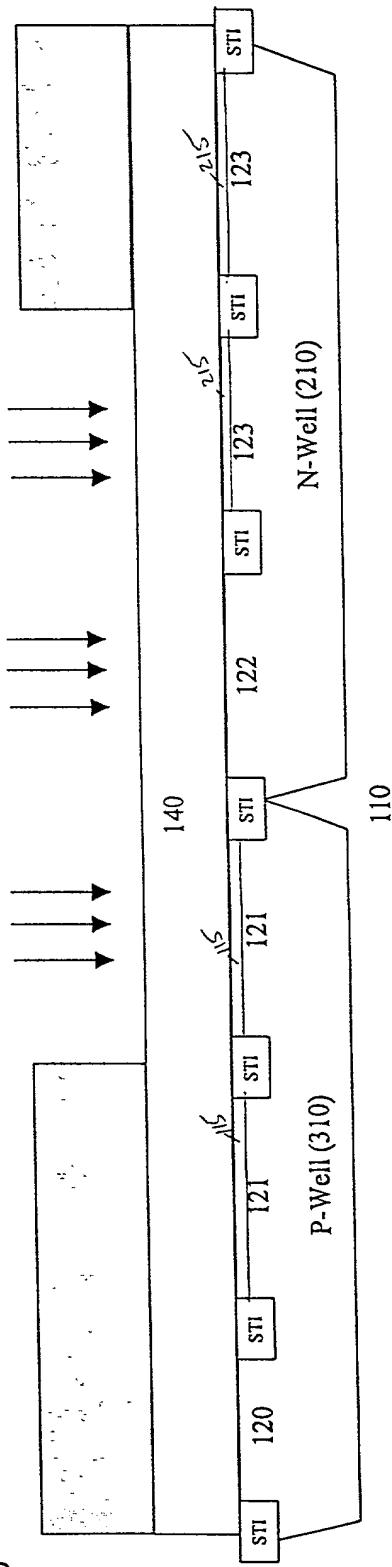


Fig. 6



N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
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Fig. 7

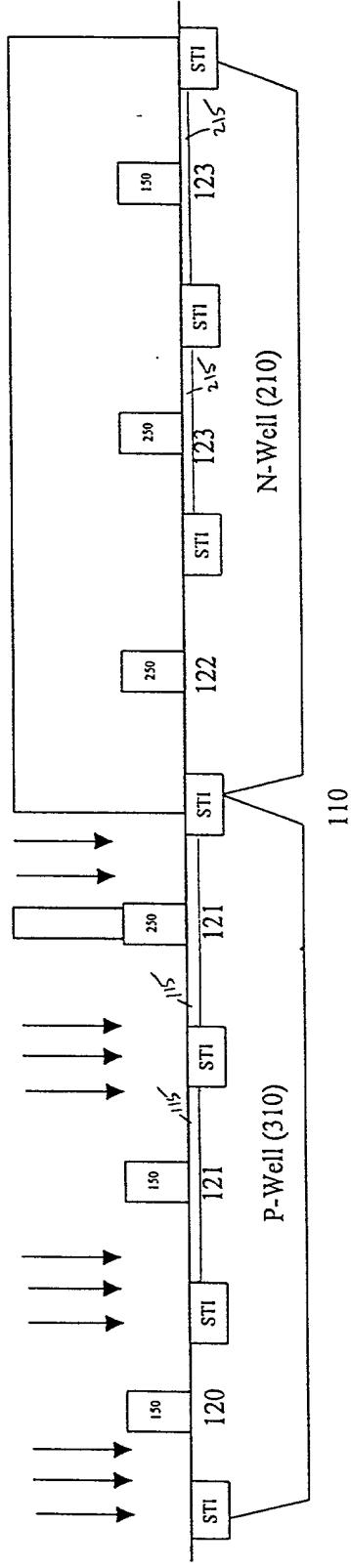
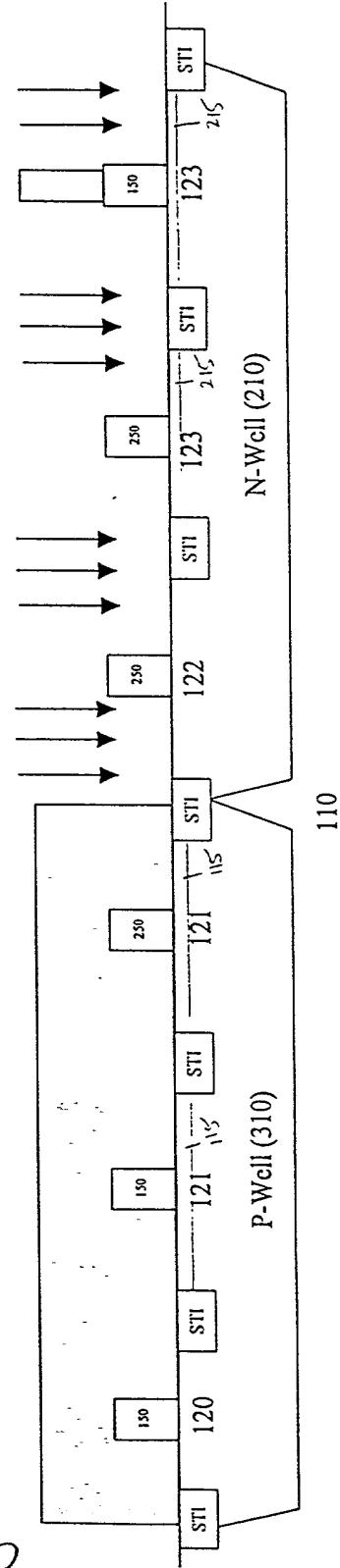


Fig. 8



N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel

Fig. 9a

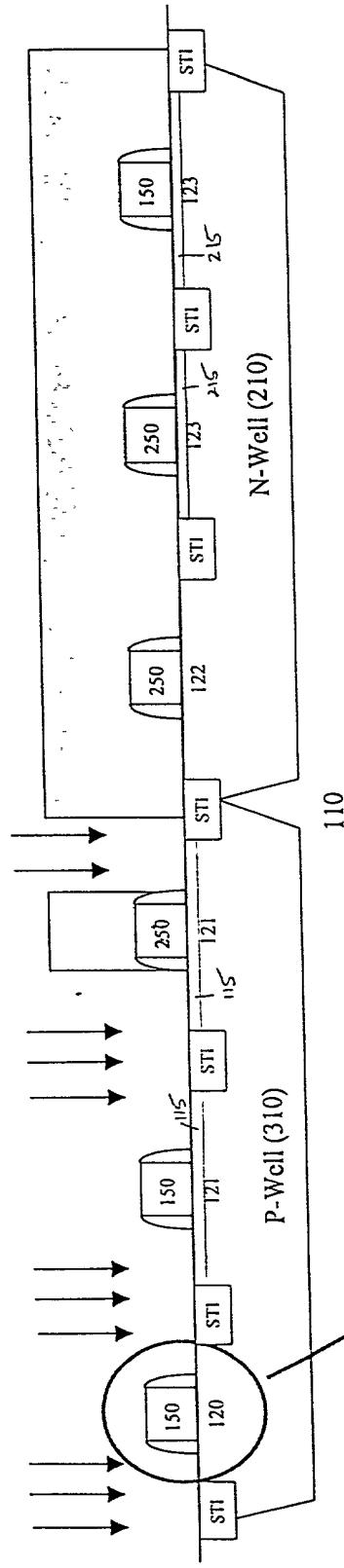
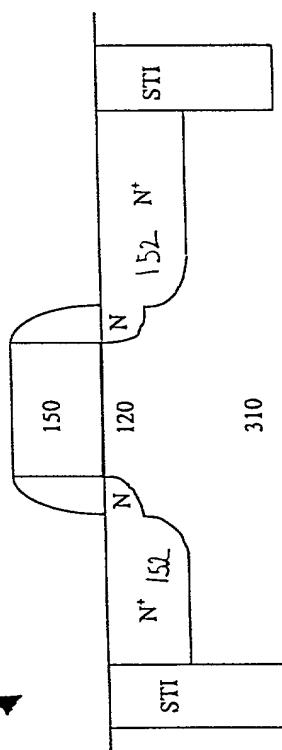


Fig. 9b



N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
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Fig. 10a

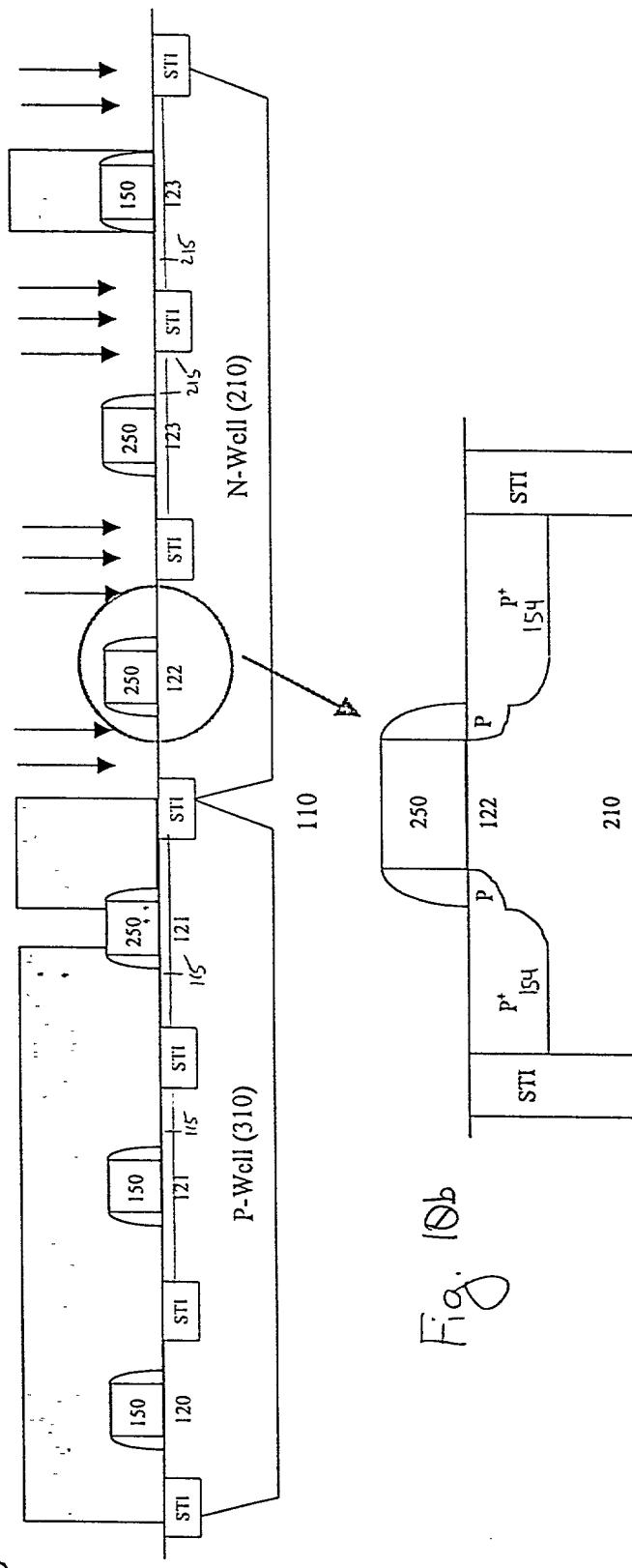


Fig. 10b

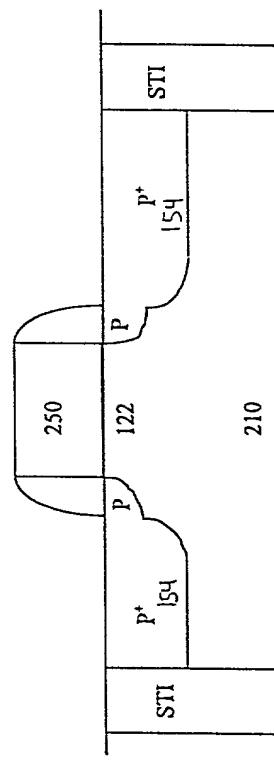
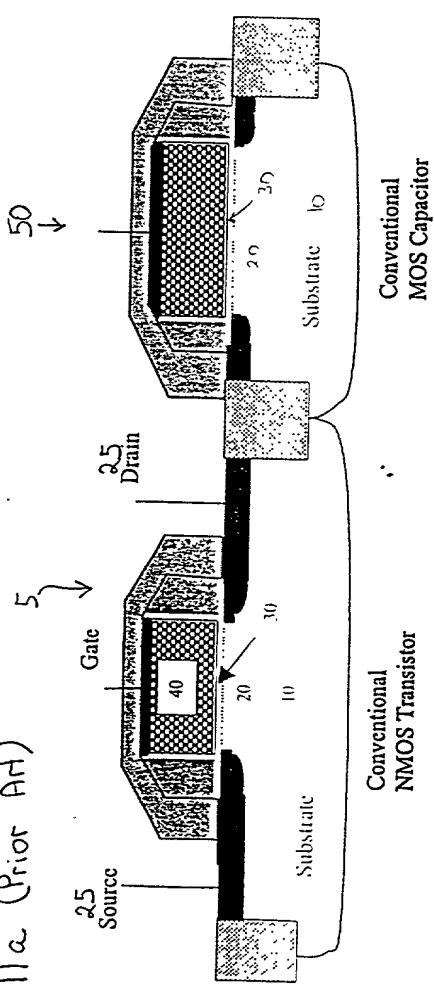


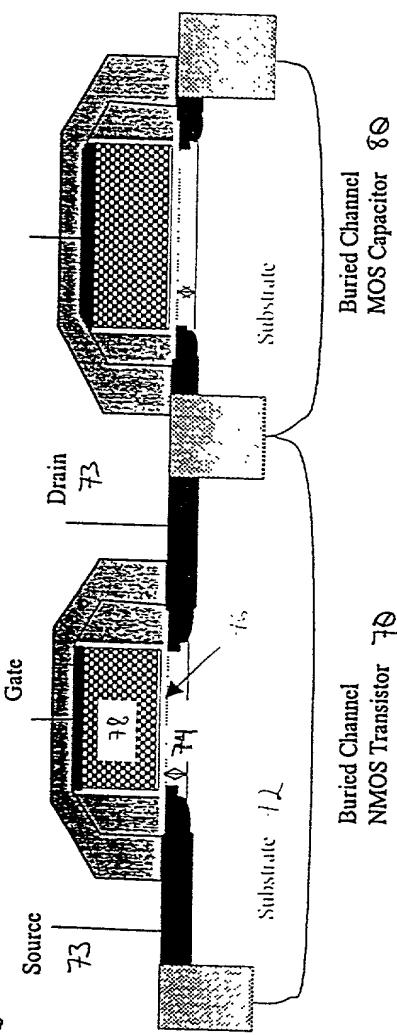
Fig. 11a (Prior Art)



Conventional  
NMOS Transistor

Conventional  
MOS Capacitor

Fig. 11b



Buried Channel  
NMOS Transistor 70

MOS Capacitor 80

Fig. 12 a  
(Prior Art)

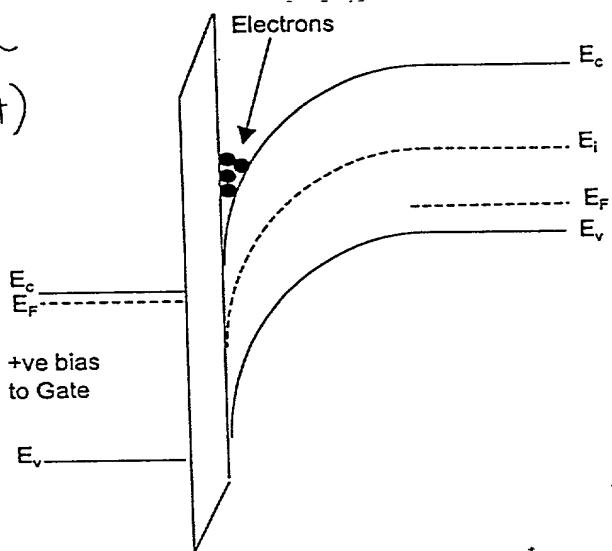


Fig. 12 b

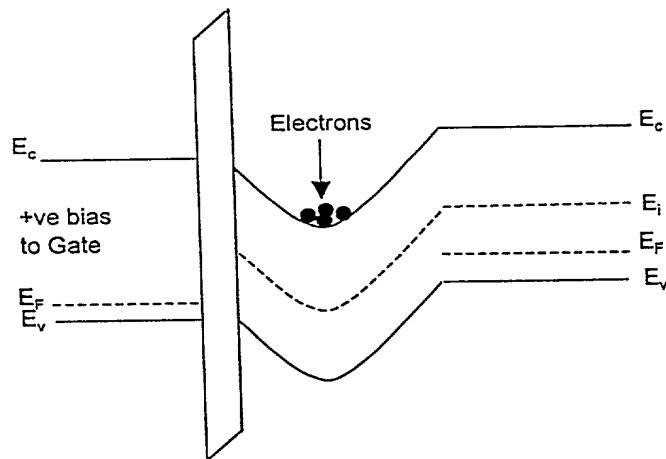


Fig 12c

